

EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|--|---|------------------|---------|------------------|
| L1 | 275 | 380/29.ccls. and @ad<"20000126" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:20 |
| L2 | 269 | 380/37.ccls. and @ad<"20000126" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:51 |
| L3 | 2 | ("6870929").PN. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/07/22 14:23 |
| L4 | 1006 | ((("6671771").PN.) or "5673319".PN. or "5673319".PN. or "5548648".PN. or "5513262".PN. or "5513262".URPN. or "5513262".PN. or "5481613".PN. or "5481613".URPN. or "5373559".PN. or "5373559".PN. or "5341426".PN. or "5341426".URPN. or "5315658".PN. or "5315658".URPN. or "5200999".PN. or "5200999".URPN. or "4607137".PN. or "4607137".URPN. or "4405829".PN. or "4405829".URPN. or "4200770".PN. or "4200770".URPN. or "5703952".PN. or "5703952".URPN. or "5673319".PN. or "5673319".URPN. or "5671284".PN. or "5671284".URPN. or "5623549".PN. or "5623549".URPN. or "5606616".PN. or "5606616".URPN. or "5214703".PN. or "5214703".URPN. or "5384848".PN. or "5384848".URPN. or "5606616".PN. or "4995082".PN. or "4933969".PN. or "4933969".URPN. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:29 |
| L5 | 59 | (L1 or L2) and L4 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:32 |

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| L6 | 4185 | ((hardware or IC or circuit or chip) with (encrypt\$3 or decrypt\$3 or crypto\$)).ab. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:40 |
| L7 | 1467 | L6 and @ad<"20000126" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:35 |
| L8 | 348 | L7 and ("380"/\$.ccls. or "713"/\$.ccls. or "726"/\$.ccls.) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:52 |
| L9 | 28 | L8 and (L1 or L2) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:46 |
| L10 | 355 | "6870929" "6549622" "6327654" "6081895" "6069956" "6038317" "6028939" "6028933" "5848154" "5687237" "5647001" RE35403 "5381480" "5317639" "5214704" "5121429" "5063596" "5038376" "4910776" "4907275" "4797928" "4634808" "4543646" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:39 |
| L11 | 226 | L10 and @ad<"20000126" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:40 |
| L12 | 115 | ((hardware or IC or circuit or chip) with (encrypt\$3 or decrypt\$3 or crypto\$)) and L11 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:41 |
| L13 | 64 | ((combinat\$3 or combin\$3 or multipl\$3 or several or many or group\$3) with (logic or algorithm or method or function\$3)) and L12 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:48 |

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| L14 | 11 | L13 and DES and block and chain\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:48 |
| L15 | 53 | L13 not L14 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:44 |
| L16 | 318 | L8 not L11 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:46 |
| L17 | 143 | ((combinat\$3 or combin\$3 or multipl\$3 or several or many or group\$3) with (logic or algorithm or method or function\$3)) and L16 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:52 |
| L18 | 16 | L17 and DES and block and chain\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:49 |
| L19 | 8850 | "digital signal processor" with (improv\$3 or high\$3 or fast\$3 or enhanc\$3 or quick\$3 or speed\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:51 |
| L20 | 3673 | L19 and @ad<"20000126" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 15:04 |
| L21 | 85 | L20 and ("380"/\$.ccls. or "713"/\$.ccls. or "726"/\$.ccls.) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:52 |

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| L22 | 67 | ((combinat\$3 or combin\$3 or multipl\$3 or several or many or group\$3) with (logic or algorithm or method or function\$3)) and L21 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:53 |
| L23 | 11 | ("6704871").URPN. | USPAT | OR | ON | 2007/07/22 15:03 |
| L24 | 0 | L23 and @ad<"20000126" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 15:05 |
| L25 | 4 | "6658578" and @ad<"20000126" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 15:05 |
| L26 | 2 | ("6810475").URPN. | USPAT | OR | ON | 2007/07/22 15:06 |



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- ☐ 1. **On the VLSI implementation of the international data encryption algorithm IDEA**
 Wolter, S.; Matz, H.; Schubert, A.; Laur, R.;
Circuits and Systems, 1995. ISCAS '95., 1995 IEEE International Symposium on
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- ☐ 2. **Security and performance optimization of a new DES data encryption chip**
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- ☐ 3. **Speech encryption based on fast Fourier transform permutation**
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- ☐ 5. **Reusable cryptographic VLSI core based on the SAFER K-128 algorithm with 251.8 Mbit/s throughput**
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- ☐ 7. **A cryptographic chip for ISDN and high speed multi-media applications**
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- ☐ 8. **An architecture of $F(2^{2N})$ multiplier for elliptic curves cryptosystem**
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- ☐ 9. **Advanced VLSI validated input security device employing data and hardware validation features**
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- ☐ 10. **Rapid prototype of a fast data encryption standard with integrity processing for cryptographic applications**
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- ☐ 11. **Radiation tolerant digital signal processor transformation**
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- ☐ 12. **CADIC: computer-aided design on internet with cryptosystem**
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- ☐ 1. **Antifuse EPROM circuit for field programmable DRAM**
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- ☐ 2. **An antifuse EPROM circuitry scheme for field-programmable repair in DRAM**
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- ☐ 1. **Synthesis of robust delay-fault-testable circuits: practice**
Devadas, S.; Keutzer, K.;
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- ☐ 2. **Design of integrated circuits fully testable for delay-faults and multifaults**
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- ☐ 3. **A single-chip FPGA implementation of the data encryption standard (DES) algorithm**
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- ☐ 4. **Non-intrusive BIST for systems-on-a-chip**
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